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APR 25 2005

**PATENT**  
**5201-27500/03-1236**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Application of:  
Hung et al.

Serial No. 10/692,091

Filed: October 23, 2003

For: MEMORY MODULE HAVING  
MIRRORED PLACEMENT OF DRAM  
INTEGRATED CIRCUITS UPON A  
FOUR LAYER PRINTED CIRCUIT  
BOARD

**Group Art Unit: 2824**

Examiner: Dang T. Nguyen

Atty. Dkt. No. 03-1236 (5201-27500)

I hereby certify that this correspondence is being transmitted via facsimile or deposited with the U.S. Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313, on the date indicated below:

04/25/2005  
Date

Kevin L. Daffert

**AMENDMENT; RESPONSE TO OFFICE ACTION MAILED FEBRUARY 9, 2005**

**Mail Stop Non-Fee Amendment**  
**Commissioner for Patents**  
**P.O. Box 1450**  
**Alexandria, VA 22313**

Dear Sir/Madam:

**Responsive to the Office Action of February 9, 2005, please amend the case as follows:**

**Amendments to the Specification** begin on page 2 of this paper.

**Amendments to the Claims** are reflected in the listing of claims which begins on page 4 of this paper.

**Remarks begin on page 8 of this paper.**